PXIe-6323 Specifications

2025-09-25

Contents

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These specifications apply to the PXIe-6323.

Revision History

Version	Date changed	Description
379188A-01	July 2025	Initial release.

Looking For Something Else?

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Related information:

- Software and Driver Downloads
- Dimensional Drawings
- Product Certifications
- Letter of Volatility
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- NI Learning Center

Definitions

Warranted specifications describe the performance of a model under stated operating conditions and are covered by the model warranty.

Characteristics describe values that are relevant to the use of the model under stated operating conditions but are not covered by the model warranty.

- ***Typical*** specifications describe the performance met by a majority of models.
- ***Nominal*** describes an attribute that is based on design, conformance testing, or

supplemental testing.

Specifications are ***Typical*** unless otherwise noted.

Conditions

Specifications are valid at 25 °C unless otherwise noted.

PXIe-6323 Pinout

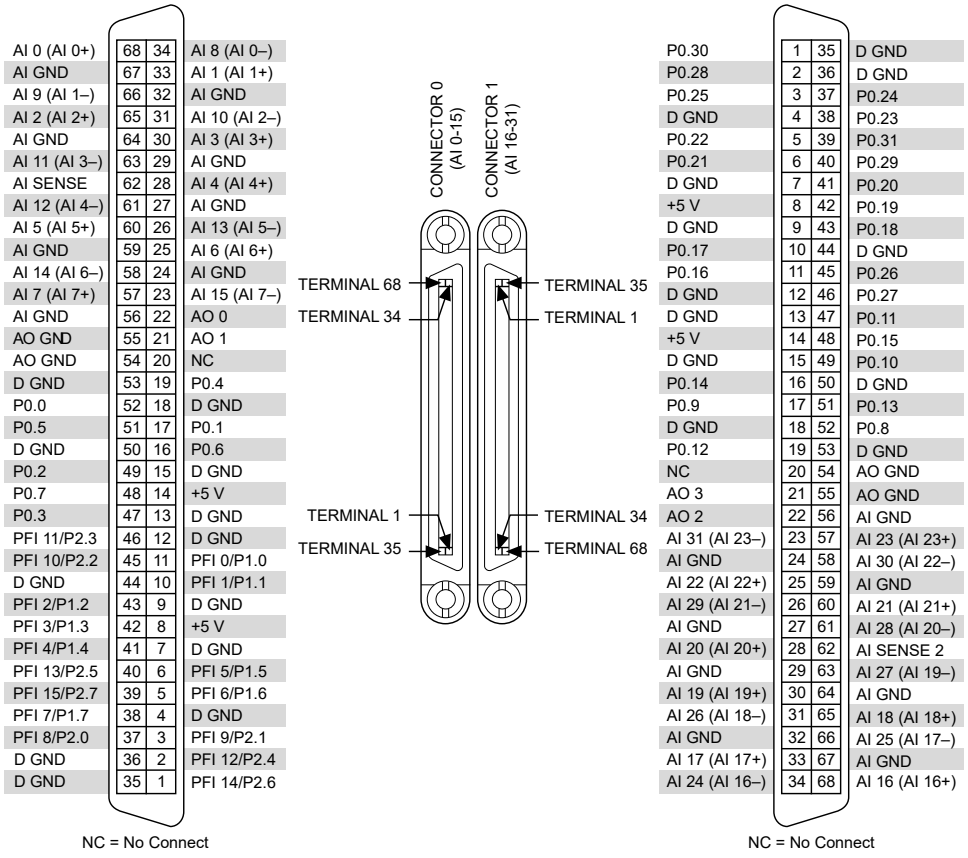


Table 1. Default Counter/Timer Terminals

Counter/Timer Signal	Default PFI Terminal
CTR 0 SRC	PFI 8
CTR 0 GATE	PFI 9
CTR 0 AUX	PFI 10

Counter/Timer Signal	Default PFI Terminal
CTR 0 OUT	PFI 12
CTR 0 A	PFI 8
CTR 0 Z	PFI 9
CTR 0 B	PFI 10
CTR 1 SRC	PFI 3
CTR 1 GATE	PFI 4
CTR 1 AUX	PFI 11
CTR 1 OUT	PFI 13
CTR 1 A	PFI 3
CTR 1 Z	PFI 4
CTR 1 B	PFI 11
CTR 2 SRC	PFI 0
CTR 2 GATE	PFI 1
CTR 2 AUX	PFI 2
CTR 2 OUT	PFI 14
CTR 2 A	PFI 0
CTR 2 Z	PFI 1
CTR 2 B	PFI 2
CTR 3 SRC	PFI 5
CTR 3 GATE	PFI 6
CTR 3 AUX	PFI 7
CTR 3 OUT	PFI 15
CTR 3 A	PFI 5
CTR 3 Z	PFI 6
CTR 3 B	PFI 7
FREQ OUT	PFI 14

Table 2. Signal Descriptions

Signal	Reference	Description
AI GND	—	Analog Input Ground—These terminals are the reference point for single-ended AI measurements in RSE mode and the bias current return point for DIFF measurements. All ground references—AI GND, AO GND, and D GND—are connected on the device, they are connected by small traces to reduce crosstalk between subsystems. Each ground has a slight difference in potential.
AI <0..31>	Varies	Analog Input Channels—For single-ended measurements, each signal is an analog input voltage channel. In RSE mode, AI GND is the reference for these signals. In NRSE mode, the reference for each AI signal is an AI SENSE. For differential measurements, AI 0 and AI 8 are the positive and negative inputs of differential analog input channel 0. Similarly, the following signal pairs also form differential input channels: AI <1,9>, AI <2,10>, and so on.
AI SENSE, AI SENSE 2	—	Analog Input Sense—In NRSE mode, the reference for each AI <0..15> signal is AI SENSE; the reference for each

Signal	Reference	Description
		AI <16..31> signal is AI SENSE 2.
AO <0..3>	AO GND	Analog Output Channels—These terminals supply voltage output.
AO GND	—	Analog Output Ground—AO GND is the reference for AO. All ground references—AI GND, AO GND, and D GND—are connected on the device. Though AI GND, AO GND, and D GND are connected on the device, they are connected by small traces to reduce crosstalk between subsystems. Each ground has a slight difference in potential.
D GND	—	Digital Ground—D GND supplies the reference for port 0, port 1, port 2 digital channels, PFI, and +5 V. All ground references—AI GND, AO GND, and D GND—are connected on the device. Though AI GND, AO GND, and D GND are connected on the device, they are connected by small traces to reduce crosstalk between subsystems. Each ground has a slight difference in potential.
P0.<0..31>	D GND	Port 0 Digital I/O Channels—You

Signal	Reference	Description
		can configure each signal individually as an input or output.
+5 V	D GND	+5 V Power Source—These terminals provide a fused +5 V power source.
PFI <0..7>/P1.<0..7>, PFI <8..15>/P2.<0..7>	D GND	Programmable Function Interface or Digital I/O Channels—Each of these terminals can be individually configured as a PFI terminal or a digital I/O terminal. As an input, each PFI terminal can be used to supply an external source for AI, AO, DI, and DO timing signals or counter/timer inputs. As a PFI output, you can route many different internal AI, AO, DI, or DO timing signals to each PFI terminal. You can also route the counter/timer outputs to each PFI terminal. As a port 1 or port 2 digital I/O signal, you can individually configure each signal as an input or output.
NC	—	No connect—Do not connect signals to this terminal.

Analog Input

Number of channels	16 differential or 32 single ended
ADC resolution	16 bits
DNL	No missing codes guaranteed
INL	Refer to the AI Absolute Accuracy section.
Sample rate	
Single channel maximum	250 kS/s
Multichannel maximum (aggregate)	250 kS/s
Minimum	No minimum
Timing resolution	10 ns
Timing accuracy	50 ppm of sample rate
Input coupling	DC
Input range	±0.2 V, ±1 V, ±5 V, ±10 V
Maximum working voltage for analog inputs (signal + common mode)	±11 V of AI GND
CMRR (DC to 60 Hz)	100 dB
Input impedance	
Device on	
AI+ to AI GND	>10 GΩ in parallel with 100 pF
AI- to AI GND	>10 GΩ in parallel with 100 pF
Device off	
AI+ to AI GND	1,200 Ω
AI- to AI GND	1,200 Ω
Input bias current	±100 pA
Crosstalk (at 100 kHz)	
Adjacent channels	-75 dB
Non-adjacent channels	-90 dB

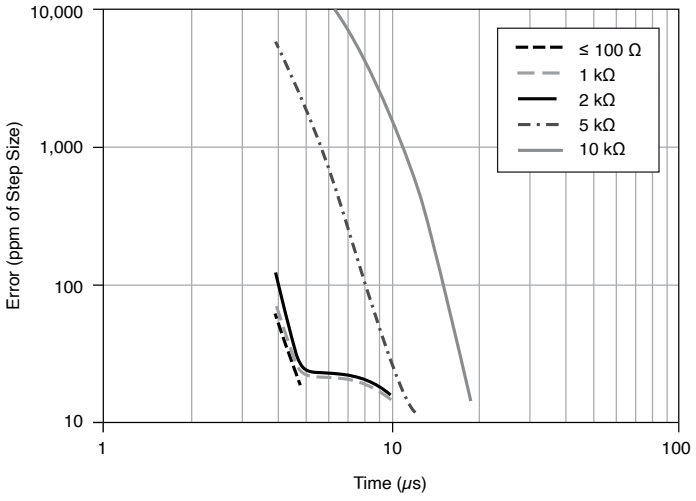
Small signal bandwidth (-3 dB)	700 kHz
Input FIFO size	4,095 samples
Scan list memory	4,095 entries
Data transfers	DMA (scatter-gather), programmed I/O
Overvoltage protection for all analog input and sense channels	
Device on	±25 V for up to two AI pins
Device off	±15 V for up to two AI pins
Input current during overvoltage condition	±20 mA maximum/AI pin

Settling Time for Multichannel Measurements

Settling time for multichannel measurements, accuracy, full-scale step, all ranges	
±90 ppm of step (±6 LSB)	4 µs convert interval
±30 ppm of step (±2 LSB)	5 µs convert interval
±15 ppm of step (±1 LSB)	7 µs convert interval

Typical Performance Graph

Figure 1. Settling Error versus Time for Different Source Impedances



AI Absolute Accuracy (Warranted)

Table 3. AI Absolute Accuracy

Nominal Range Positive Full Scale (V)	Nominal Range Negative Full Scale (V)	Residual Gain Error (ppm of Reading)	Residual Offset Error (ppm of Range)	Offset Tempco (ppm of Range/°C)	Random Noise, σ (µVrms)	Absolute Accuracy at Full Scale (µV)
10	-10	65	13	24	229	2,200
5	-5	72	13	25	118	1,140
1	-1	78	17	37	26	257
0.2	-0.2	105	27	93	12	69

Note ***Absolute Accuracy at Full Scale*** is determined using the following assumptions:

- TempChangeFromLastExternalCal = 10 °C
- TempChangeFromLastInternalCal = 1 °C
- NumberOfReadings = 10,000
- CoverageFactor = 3 σ

For more information about absolute accuracy at full scale, refer to the ***AI Absolute Accuracy*** section.

Note Accuracies listed are valid for up to two years from the device external calibration.

Gain tempco	7.3 ppm/°C
Reference tempco	5 ppm/°C
INL error	60 ppm of range

AI Absolute Accuracy Equation

$$\textit{AbsoluteAccuracy} = \textit{Reading} \cdot \textit{(GainError)} + \textit{Range} \cdot \textit{(OffsetError)} + \textit{NoiseUncertainty}$$

- $\textit{GainError} = \textit{ResidualGainError} + \textit{GainTempco} \cdot \textit{(TempChangeFromLastInternalCal)} + \textit{ReferenceTempco} \cdot \textit{(TempChangeFromLastExternalCal)}$
- $\textit{OffsetError} = \textit{ResidualOffsetError} + \textit{OffsetTempco} \cdot \textit{(TempChangeFromLastInternalCal)} + \textit{INLError}$
- $\textit{NoiseUncertainty} = \frac{\text{Random Noise} \cdot 3}{\sqrt{10,000}}$
  for a coverage factor of 3 σ and averaging 10,000 points.

AI Absolute Accuracy Example

For example, on the 10 V range, the absolute accuracy at full scale is as follows:

- ***GainError***: 65 ppm + 7.3 ppm · 1 + 5 ppm · 10 = 122 ppm
- ***OffsetError***: 13 ppm + 24 ppm · 1 + 60 ppm = 97 ppm
- ***NoiseUncertainty***:
  $\frac{229\ \mu V \cdot 3}{\sqrt{10,000}}$
  = 6.9 µV
- ***AbsoluteAccuracy***: 10 V · (***GainError***) + 10 V · (***OffsetError***) + ***NoiseUncertainty*** = 2,220 µV

Analog Output

Number of channels	4
DAC resolution	16 bits
DNL	±1 LSB
Monotonicity	16 bit guaranteed
Maximum update rate	
1 channel	900 kS/s

2 channels	840 kS/s per channel
3 channels	775 kS/s per channel
4 channels	719 kS/s per channel
Timing accuracy	50 ppm of sample rate
Timing resolution	10 ns
Output range	±10 V
Output coupling	DC
Output impedance	0.2 Ω
Output current drive	±5 mA
Overdrive protection	±15 V
Overdrive current	15 mA
Power-on state	±20 mV
Power-on/off glitch	2 V for 500 ms
Output FIFO size	8,191 samples shared among channels used
Data transfers	DMA (scatter-gather), programmed I/O
AO waveform modes	Non-periodic waveform, periodic waveform regeneration mode from onboard FIFO, periodic waveform regeneration from host buffer including dynamic update
Settling time, full-scale step, 15 ppm (1 LSB)	6 µs
Slew rate	15 V/µs
Glitch energy	
Magnitude	100 mV
Duration	2.6 µs

AO Absolute Accuracy (Warranted)

Table 4. AO Absolute Accuracy

Nominal Range Positive Full Scale	Nominal Range Negative Full Scale	Residual Gain Error (ppm of Reading)	Gain Tempco (ppm/°C)	Reference Tempco (ppm/°C)	Residual Offset Error (ppm of Range)	Offset Tempco (ppm of Range/°C)	INL Error (ppm of Range)	Absolute Accuracy at Full Scale (µV)
10	-10	80	11.3	5	53	4.8	128	3,271

Note Absolute accuracy at full-scale numbers is valid immediately following self calibration and assumes the device is operating within 10 °C of the last external calibration.

Note Accuracies listed are valid for up to two years from the device external calibration.

AO Absolute Accuracy Equation

$$AbsoluteAccuracy = OutputValue \cdot (GainError) + Range \cdot (OffsetError)$$

- $GainError = ResidualGainError + GainTempco \cdot (TempChangeFromLastInternalCal) + ReferenceTempco \cdot (TempChangeFromLastExternalCal)$
- $OffsetError = ResidualOffsetError + OffsetTempco \cdot (TempChangeFromLastInternalCal) + INLError$

Digital I/O/PFI

Static Characteristics

Number of channels	48 total, 32 (P0.<0..31>), 16 (PFI <0..7>/P1, PFI <8..15>/P2)
Ground reference	D GND

Direction control	Each terminal individually programmable as input or output
Pull-down resistor	50 kΩ typical, 20 kΩ minimum
Input voltage protection	±20 V on up to two pins

Notice Stresses beyond those listed under the ***Input voltage protection*** specification may cause permanent damage to the device.

Waveform Characteristics (Port 0 Only)

Terminals used	Port 0 (P0.<0..31>)
Port/sample size	Up to 32 bits
Waveform generation (DO) FIFO	2,047 samples
Waveform acquisition (DI) FIFO	255 samples
DO or DI Sample Clock frequency	0 to 1 MHz, system and bus activity dependent
Data transfers	DMA (scatter-gather), programmed I/O
Digital line filter settings	160 ns, 10.24 µs, 5.12 ms, disable

PFI/Port 1/Port 2 Functionality

Functionality	Static digital input, static digital output, timing input, timing output
Timing output sources	Many AI, AO, counter, DI, DO timing signals
Debounce filter settings	90 ns, 5.12 µs, 2.56 ms, custom interval, disable; programmable high and low transitions; selectable per input

Recommended Operating Conditions

Input high voltage (V_{IH})	
Minimum	2.2 V

Maximum	5.25 V
Input low voltage (V_{IL})	
Minimum	0 V
Maximum	0.8 V
Output high current (I_{OH})	
P0.<0..731	-24 mA maximum
PFI <0..15>/P1/P2	-16 mA maximum
Output low current (I_{OL})	
P0.<0..31>	24 mA maximum
PFI <0..15>/P1/P2	16 mA maximum

Digital I/O Characteristics

Positive-going threshold (VT+)	2.2 V maximum
Negative-going threshold (VT-)	0.8 V minimum
Delta VT hysteresis (VT+ - VT-)	0.2 V minimum
I_{IL} input low current (V_{IN} = 0 V)	-10 µA maximum
I_{IH} input high current (V_{IN} = 5 V)	250 µA maximum

Figure 2. P0.<0..31>: I_{OH} versus V_{OH}

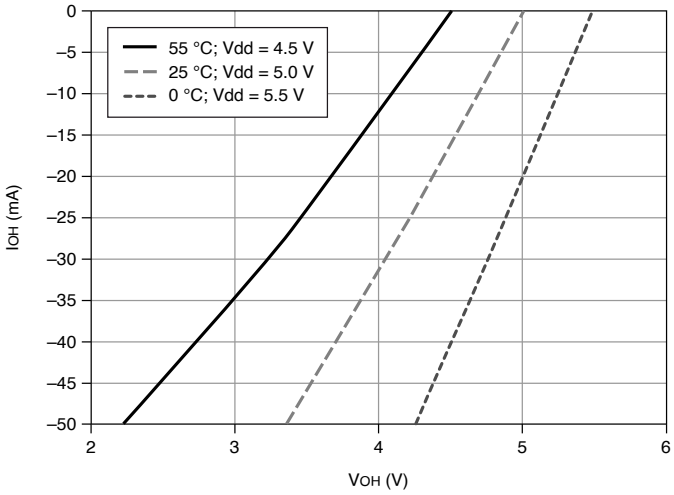


Figure 3. P0.<0..31>: I_{OL} versus V_{OL}

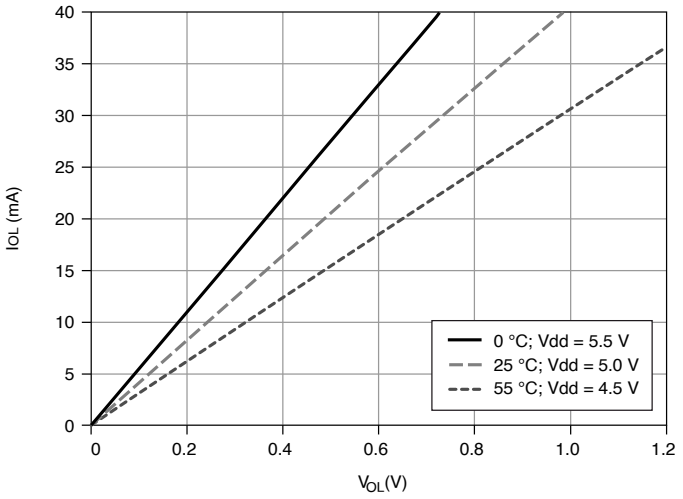


Figure 4. PFI <0..15>/P1/P2: I_{OH} versus V_{OH}

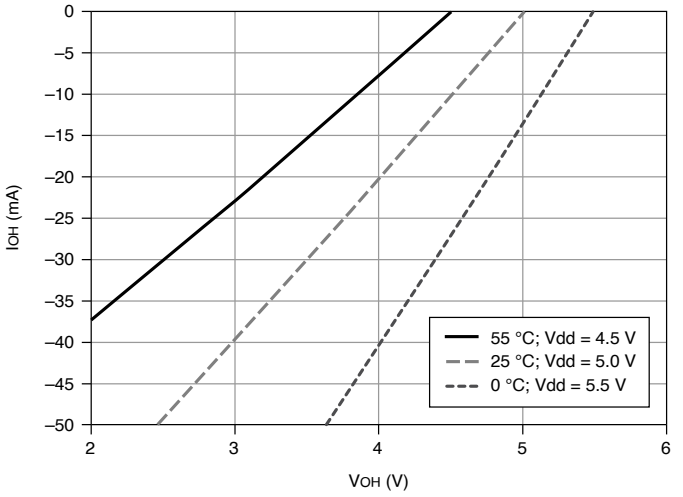
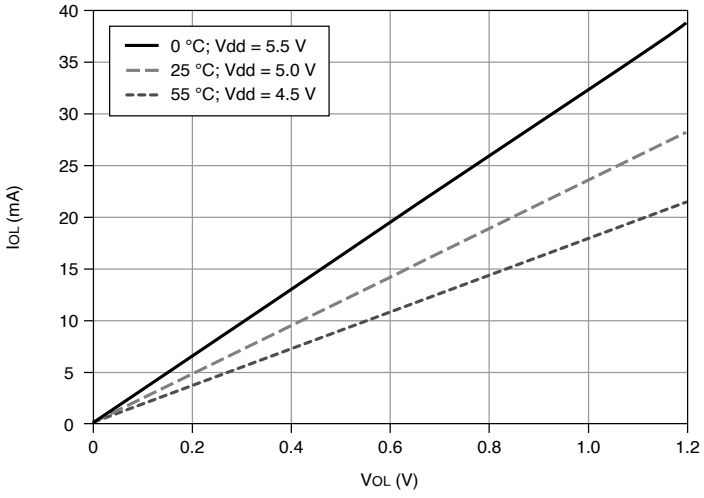


Figure 5. PFI <0..15>/P1/P2: I_{OL} versus V_{OL}



General-Purpose Counters

Number of counter/timers	4
Resolution	32 bits
Counter measurements	Edge counting, pulse, pulse width, semi-period, period, two-edge separation
Position measurements	X1, X2, X4 quadrature encoding with Channel Z reloading; two-pulse encoding
Output applications	Pulse, pulse train with dynamic updates, frequency division, equivalent time sampling
Internal base clocks	100 MHz, 20 MHz, 100 kHz
External base clock frequency	0 MHz to 25 MHz; 0 MHz to 100 MHz on PXIe_DSTAR<A,B>
Base clock accuracy	50 ppm
Inputs	Gate, Source, HW_Arm, Aux, A, B, Z, Up_Down, Sample Clock
Routing options for inputs	Any PFI, PXIe_DSTAR<A,B>, PXI_TRIG<0..7>, PXI_STAR, many internal signals
FIFO	127 samples per counter
Data transfers	Dedicated scatter-gather DMA controller for each counter/timer, programmed I/O

Frequency Generator

Number of channels	1
Base clocks	20 MHz, 10 MHz, 100 kHz
Divisors	1 to 16
Base clock accuracy	50 ppm

Phased-Locked Loop (PLL)

Number of PLLs	1

Table 5. Reference Clock Locking Frequencies

Reference Signal	Locking Input Frequency (MHz)
PXIe_DSTAR<A,B>	10 MHz, 20 MHz, 100 MHz
PXI_STAR	10 MHz, 20 MHz
PXIe_CLK100	100 MHz
PXI_TRIG <0..7>	10 MHz, 20 MHz
PFI <0..15>	10 MHz, 20 MHz

Output of PLL	100 MHz Timebase; other signals derived from 100 MHz Timebase including 20 MHz and 100 kHz Timebases

External Digital Triggers

Source	Any PFI, PXIe_DSTAR<A,B>, PXI_TRIG<0..7>, PXI_STAR
Polarity	Software-selectable for most signals
Analog input function	Start Trigger, Reference Trigger, Pause Trigger, Sample Clock, Convert Clock, Sample Clock Timebase
Analog output function	Start Trigger, Pause Trigger, Sample Clock, Sample Clock Timebase
Counter/timer functions	Gate, Source, HW_Arm, Aux, A, B, Z, Up_Down, Sample Clock
Digital waveform generation (DO) function	Start Trigger, Pause Trigger, Sample Clock, Sample Clock Timebase
Digital waveform acquisition (DI) function	Start Trigger, Reference Trigger, Pause Trigger, Sample Clock, Sample Clock Timebase

Device-to-Device Trigger Bus

Input source	PXI_TRIG <0..7>, PXI_STAR, PXIe_DSTAR<A,B>
Output destination	PXI_TRIG <0..7>, PXIe_DSTARC
Output selections	10 MHz Clock; frequency generator output; many internal signals

Debounce filter settings	90 ns, 5.12 µs, 2.56 ms, custom interval, disable; programmable high and low transitions; selectable per input

Current Limits

Notice Exceeding the current limits may cause unpredictable device behavior.

+5 V terminal (connector 0)	1 A maximum[1][1]
+5 V terminal (connector 1)	1 A maximum[1]
P0/PFI/P1/P2 and +5 V terminals combined	2 A maximum

Bus Interface

Form factor	x1 PXI Express peripheral module, specification rev 1.0 compliant
Slot compatibility	x1 and x4 PXI Express or PXI Express hybrid slots
DMA channels	8, can be used for analog input, analog output, digital input, digital output, counter/timer 0, counter/timer 1, counter/timer 2, counter/timer 3

Safety Voltages

Table 6. Rated Voltages

AI+ or AI- to GND	±11 V DC
AO to GND	±10 V DC
DIO-to-GND	+5 V DC
+5V pin to GND	+5 V DC

1. Has self-resetting fuse that opens when current exceeds this specification.

Caution Any external sources must be limited to not exceed these maximum rated voltages.

Attention Les sources externes doivent être limitées pour ne pas dépasser ces tensions nominales maximales.

Current Ratings

DIO Maximum continuous current	Per channel	±10 mA
	Sum of all channels	±160 mA
AO Maximum continuous current	Per channel	2 mA

Caution

Any external sources must be limited to not exceed these maximum rated currents.

Attention

Les sources externes doivent être limitées pour ne pas dépasser ces tensions nominales maximales.

Measurement Category

This product is rated for Measurement Category I (or other non-MAINS circuits).

Caution Do not connect the product to signals or use for measurements within Measurement Categories II, III, or IV.

Attention Ne pas connecter le produit à des signaux dans les catégories de

mesure II, III ou IV et ne pas l'utiliser pour effectuer des mesures dans ces catégories.

Measurement Category I is for measurements performed on circuits not directly connected to the electrical distribution system referred to as ***MAINS*** voltage. MAINS is a hazardous live electrical supply system that powers equipment. This category is for measurements of voltages from specially protected secondary circuits. Such voltage measurements include signal levels, special equipment, limited-energy parts of equipment, circuits powered by regulated low-voltage sources, and electronics.

Note Measurement Categories CAT I and CAT O are equivalent. These test and measurement circuits are for other circuits not intended for direct connection to the MAINS building installations of Measurement Categories CAT II, CAT III, or CAT IV.

Environmental Guidelines

Notice Failure to follow the mounting instructions in the product documentation can cause temperature derating.

Notice This product is intended for use in indoor applications only.

Environmental Characteristics

Temperature	Operating	0 °C to 55 °C
	Storage	-40 °C to 70 °C
Humidity	Operating	10% RH to 90% RH, noncondensing
	Storage	5% RH to 95% RH, noncondensing
Pollution Degree		2
Maximum altitude		2000 m

Power Requirements

Table 7. Power Specifications

PXIe Bus	Voltage/current rating	0.48 A at 3.3 V DC 1.75 A at 12 V DC
	Power rating	21.4 W

Physical Characteristics

Table 8. Dimensions and Weight

Device dimensions	3U, one-slot, PXI Express/Compact PCI Express module
Weight	198 g (7.0 oz)

Calibration

Recommended warm-up time	15 minutes
Calibration interval	2 years